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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.
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09/450,491 11/29/99 YOKOYAMA

R YKI-0024

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TM02/0713

EXAMINER

ABDUL SELAM, A

ART UNIT

PAPER NUMBER

2674

DATE MAILED:

07/13/01

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks

# Office Action Summary

Application No.  
09/450,491

Applicant(s)

Ryoichi Yokoyama

Examiner  
Abbas Abdulsalam

Group Art Unit  
2674



☐ Responsive to communication(s) filed on \_\_\_\_\_.

☐ This action is **FINAL**.

☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11; 453 O.G. 213.

A shortened statutory period for response to this action is set to expire 3 month(s), or thirty days, whichever is longer, from the mailing date of this communication. Failure to respond within the period for response will cause the application to become abandoned. (35 U.S.C. § 133). Extensions of time may be obtained under the provisions of 37 CFR 1.136(a).

## Disposition of Claims

☒ Claim(s) 1-16 is/are pending in the application.

Of the above, claim(s) \_\_\_\_\_ is/are withdrawn from consideration.

☐ Claim(s) \_\_\_\_\_ is/are allowed.

☒ Claim(s) 1-16 is/are rejected.

☐ Claim(s) \_\_\_\_\_ is/are objected to.

☐ Claims \_\_\_\_\_ are subject to restriction or election requirement.

## Application Papers

☐ See the attached Notice of Draftsperson's Patent Drawing Review, PTO-948.

☐ The drawing(s) filed on \_\_\_\_\_ is/are objected to by the Examiner.

☐ The proposed drawing correction, filed on \_\_\_\_\_ is ☐ approved ☐ disapproved.

☐ The specification is objected to by the Examiner.

☐ The oath or declaration is objected to by the Examiner.

## Priority under 35 U.S.C. § 119

☐ Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d).

☐ All ☐ Some\* ☐ None of the CERTIFIED copies of the priority documents have been  
☐ received.

☐ received in Application No. (Series Code/Serial Number) \_\_\_\_\_.

☐ received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\*Certified copies not received: \_\_\_\_\_.

☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).

## Attachment(s)

☒ Notice of References Cited, PTO-892

☐ Information Disclosure Statement(s), PTO-1449, Paper No(s). \_\_\_\_\_

☐ Interview Summary, PTO-413

☐ Notice of Draftsperson's Patent Drawing Review, PTO-948

☐ Notice of Informal Patent Application, PTO-152

--- SEE OFFICE ACTION ON THE FOLLOWING PAGES ---

Art Unit: 2674

## **DETAILED ACTION**

### **Claim Rejections 35 U.S.C. 102**

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1, 3-6, 9, and 11-14 are rejected under U.S.C. 102(b) as being anticipated by Tornqvist (USPN 5133036).

Regarding claims 1 and 9, Tornqvist teaches the first electrode structure (9), luminous multilayered thin film structure (10, 11, 12). second electrode structure (13, 14). See fig 2, and 3. Tornqvist teaches about transparent second electrode structure containing parallel electrode conductors. Tornqvist also teaches that the second electrode structure is provided with a narrow stripe (14) of high electrical conductivity. Moreover, Tornqvist teaches about electroluminescent thin film structure of a display unit and the use of emission filter material. See column 1, lines 7-13, and Column 3, lines 55-65.

Regarding claims 3, and 11, Tornqvist teaches about a thin-film electrode layer, which is partly metallic or a metal alloy. See column 2, lines 22-33.

Regarding claims 4 and 12, see Fig 3 (13, 14)

Art Unit: 2674

Regarding claims 5, and 13, Tornqvist teaches about layers (10, 11, 12) between first electrode (9) and second electrode (13, 14). See Column 3, lines 45-50, and Fig 2.

Regarding claims 6 and 14, Tornqvist teaches about photolithography and HCL etching. See, column 42-45, and 60-62.

### **Claim Rejections - 35 U.S.C. 103**

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 2, 7-8 and 15-16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Tornqvist in view of Ishiguro et al. (USPN 6146928).

Tornqvist has been described above. However, Tornqvist does not teach a type of thin film transistors containing a polycrystalline silicon layer, and does not disclose an external signal supply device connected to light emission panel. Also, Tornqvist does not teach conducting materials of conductors in connection to a gate electrode, drain electrode, and source electrode. Ishiguro on the other hand teaches about a thin film transistor containing a polycrystalline silicon layer (17) with respect to gate electrode (19), source, and drain regions (20), See Fig 2(a), and Fig 2(b), column 4, lines 60-67, and column 5, lines 1-10. In addition Ishiguro teaches about external power source (1010) connected to liquid crystal panel (1006). See Fig 17. Therefore, it

Art Unit: 2674

would have been obvious to one having skill in the art at the time the invention was made to modify Tornqvist's thin film matrix structure to include a polycrystalline silicon layer, use the same material for conductors as well as transistors, and connect an external power source to a light emission panel. One would have been motivated in view of Ishiguro that the desired polycrystalline silicon layer, external signal supply device, and the conductive material for the three electrodes (gate, drain, and source) are equivalent to a polycrystalline silicon layer, electric power source, and composing materials of a thin film transistor. The use of polycrystalline silicon layer, and electric power source, helps achieve a reliable thin film transistor as taught by Ishiguro.

### **Conclusion**

3. The prior art made of record and not relied upon is considered to applicant's disclosure.

The following arts are cited for further references.

U.S. Pat No. 5,598,059 to Sun et al.

U.S. Pat No. 5,767,623 to Friedman et al.

U.S. Pat No. 5,821,688 to Shanks et al.

U.S. Pat No. 5,831,699 to Wright et al.

U.S. Pat No. 6,125, 027 to Klee et al.

4. Any response to this actions should be mailed to

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Art Unit: 2674

Any inquires concerning this matter should be addressed to Abbas Abdulsalam Tel (703) 305-8591. The examiner can normally be reached on Monday through Friday (9:00-5:30).


If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Hjerpe can be reached on (703) 305-4709. The fax number for the organization where this application or proceeding is assigned is (703) 308-6606.

Any inquires of a general nature or relating to the status of this application should be directed to the receptionist whose telephone number is (703) 305-4700.

Abbas abdulsalam

Examiner

Art Unit 2674



**RICHARD HJERPE**  
**SUPERVISORY PATENT EXAMINER**  
**TECHNOLOGY CENTER 2600**